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|                        |                  |
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| Application Number     | 09989762         |
| Filing Date            | 2001-11-19       |
| First Named Inventor   | Kenneth Y. Ogami |
| Art Unit               | 2123             |
| Examiner Name          | Osborne, Luke R. |
| Attorney Docket Number | CD01175M         |

**U.S. PATENTS**

| Examiner Initial* | Cite No | Patent Number | Kind Code <sup>1</sup> | Issue Date | Name of Patentee or Applicant of cited Document | Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear |
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| Examiner Initial* | Cite No | Publication Number | Kind Code <sup>1</sup> | Publication Date | Name of Patentee or Applicant of cited Document | Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear |
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| /L.O./             | 6       | Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot;" <del>Nemecek</del> , 44 pages<br><b>Nemecek et al.</b>   | <input type="checkbox"/> |
| /L.O./             | 7       | Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer;" <del>Nemecek</del> , 47 pages<br><b>Nemecek et al.</b>   | <input type="checkbox"/> |
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| /L.O./ | 9  | Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer," <del>Nemecek</del> , 46 pages<br><b>Nemecek et al.</b>   | <input type="checkbox"/> |
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| /L.O./ | 12 | Application Number 10/001,477 (CD01203M): "Breakpoint Control in an In-Circuit Emulation System," <del>Roe</del> ; 43 pages<br><b>Roe et al.</b>   | <input type="checkbox"/> |
| /L.O./ | 13 | Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller," <del>Sullam</del> , 34 pages<br><b>Sullam et al.</b>  | <input type="checkbox"/> |
| /L.O./ | 14 | Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller," <del>Kutz</del> ; 44 pages<br><b>Kutz et al.</b>                                 | <input type="checkbox"/> |
| /L.O./ | 15 | Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit," Snyder; 25 pages  | <input type="checkbox"/> |
| /L.O./ | 16 | Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller," <del>Kutz</del> ; 37 pages<br><b>Kutz et al.</b>  | <input type="checkbox"/> |
| /L.O./ | 17 | Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," <del>Sullam</del> ; 49 pages<br><b>Sullam et al.</b> | <input type="checkbox"/> |
|        | 18 | Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit," Mar; 31 pages  | <input type="checkbox"/> |
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|--------|----|--|--------------------------|
| /L.O./ | 20 | GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM; "Hardware Emulation for Functional Verification of K6", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996, 4 pages | <input type="checkbox"/> |
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| /L.O./ | 24 | Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability;" <del>Mar</del> , 28 pages<br><b>Mar et al.</b>   | <input type="checkbox"/> |
| /L.O./ | 25 | Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks;" Snyder; 28 pages  | <input type="checkbox"/> |
| /L.O./ | 26 | Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode;" Snyder; 31 pages   | <input type="checkbox"/> |
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